ST. ANNE'S COLLEGE OF ENGINEERING AND TECHNOLOGY
(Approved by AICTE, New Delhi. Affiliated to Anna University, Chennai)
ANGUCHETTYPALAYAM, PANRUTI – 607 106.

Academic Year
2020-21
Participative Learning

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Academic Year 2020-21

Participative Learning

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MINI PROJECT EXPO

ST. ANNE'S

College of Engineering & Technology

(A unit of sisters of St. Anne, Tiruchirapalli)

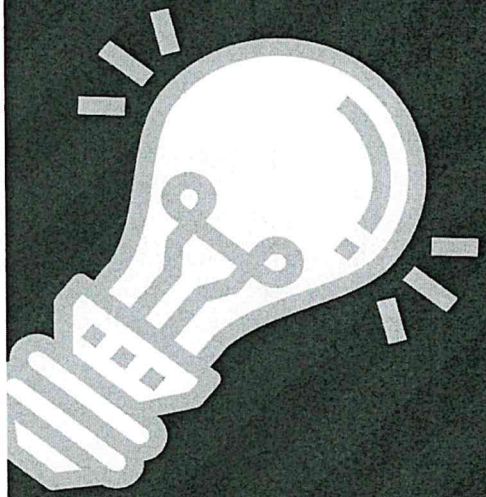
(An ISO 9001:2015 Certified Institution)

Approved by AICTE, Delhi & Affiliated to Anna University, Chennai

Website: www.stannescet.ac.in | Email ID: stannescet@gmail.com | Phone No: 04142 - 241661, 242661

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

organizes



ELECTRONIC CIRCUIT DESIGN CONTEST

(Hardware& Software)

Submit your innovative circuit idea and win exciting prizes

Date : 24.09.2021

Time : 01:30PM to 04:00PM

Venue : VLSI Lab

Help Contact

Mrs. B. Mary Amala Jenni, AP/ECE - 8248061567

Mr. V. Venkatesan, AP/ECE - 8939150098

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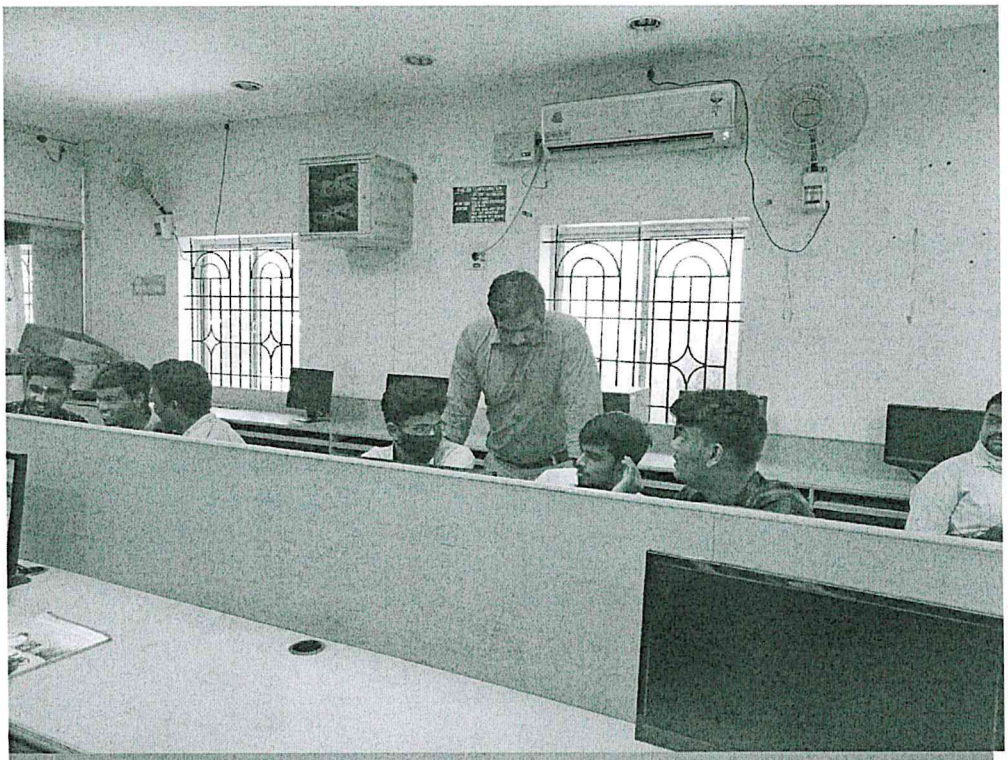
DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

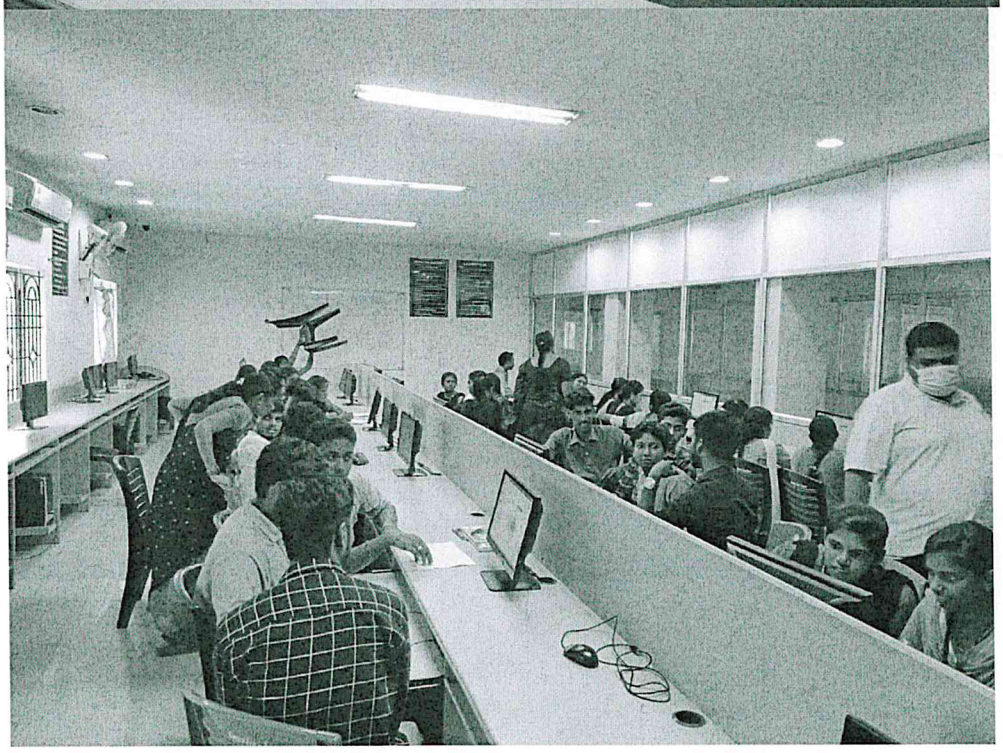
EVENT: PROJECT DESIGN CONTEST **YEAR/SEM: II,III,IV/III,V,VII**

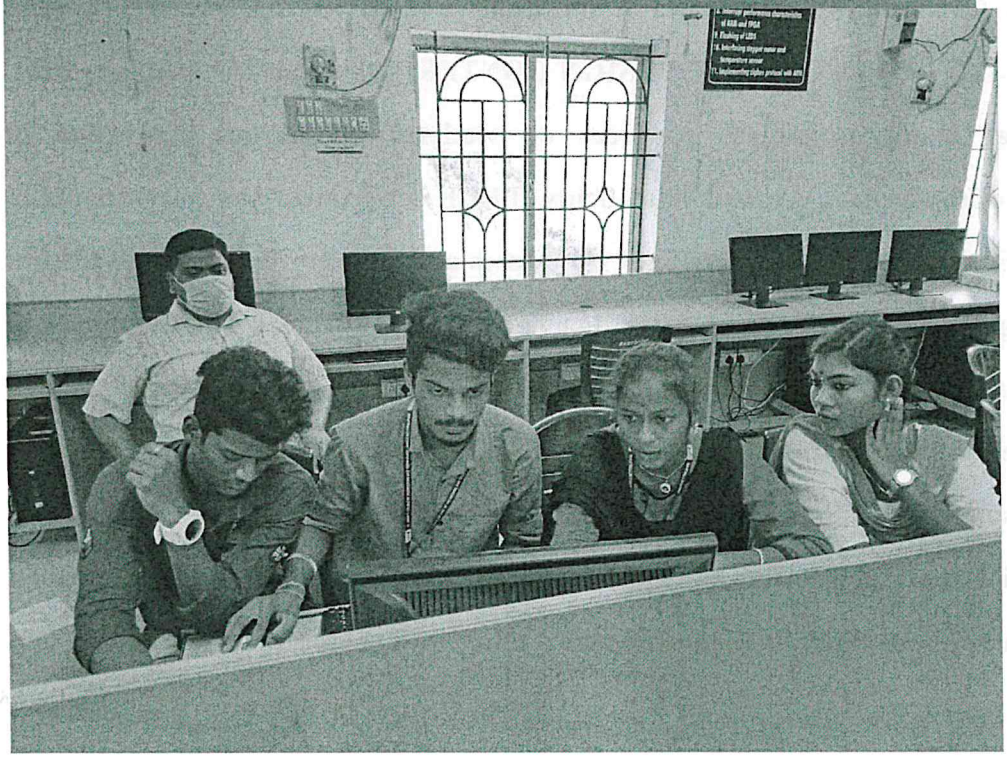
TITLE: Electronic Circuit Design Contest **DATE : 25.10.2021**

STUDENTS BATCH LIST

S. NO	REGISTER NO	NAME OF THE STUDENT	TITLE OF THE PROJECT
1	422119106001	Anandhi M	Blind Stick
	422118106003	Kaveri K	
	422118106005	Manju R	
	422120106001	Akash R	
2	422119106002	Cindrella Pascal S	IOT Based Moisture Detection System
	422118106007	Parameswari G	
	422118106009	Preethi R	
	422120106002	Anilkumar A	
3	422119106003	Kogila R	Barrier Elusion Robot
	422118106012	Ramya V	
	422118106014	Sandhiya R	
	422120106003	Leela G	
4	422119106005	Manoj S	Social Distancing ID Card
	422118106001	Abinesh M	
	422118106013	Ravichandiran P	
	422120106004	Nivetha U	
5	422119106006	Pradeepa S	Automatic Road Reflector Light
	422118106017	Seethalakshmi G	
	422118106018	Selva Praveena S	
	422120106005	Rajasri S	
6	422119106007	Ranjithkumar M	Fire Detection and Alarm
	422118106021	Sivasakthi P	
	422118106020	Sineka R	
	422120106006	Shanmugapriya N	







The Department of Electronics and Communication Engineering (ECE) in association with Electronics and Communication Scientia Association (ECSA) has organized a project contest on " ELECTRONIC CIRCUIT DESIGN CONTEST-2021" on 25th October, 2021. The contest was mainly organized to focus on students' ideas and innovation in implementation of the project. Around 40 students of ECE department has participated in this workshop. Students were separated into 10 batches and in every batch students from all 3 year were grouped together. Each batch displayed one mini project. The chief guest of the contest was Mrs. S.Saranya, Alumini(2009-2013)/ECE, Direct Assistant, Health Manpower Development Institute, Villupuram. The chief guest along with Rev.Sr.Dr.Y.Yesuthangam, Secretary, Dr.R.Arokiadass, Principal and Sr. Punitha Jilt, Vice Principal visited all the projects and appreciated their efforts. At the end of the session the chief guest announced the winners and awarded them with medals and certificates.

Dr. R. AROKIADASS, M.E., Ph.D.,
Principal,
St. Anne's College of Engineering & Technology,
ANGUCHETTYPALAYAM,
Siruvathur-(Post), Panruti-(T.k),
Cuddalore-(Dist), Pin: 607 110.

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STUDENT SEMINAR

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

CIRCULAR
(2020-2021)

CIR. No.: SACET/ECE/CIR/ **Date:04.07.2020**

It is to be informed that the **Student Seminar** will be held for III/V year/sem students at Digital LAb. All interested staff and students are invited for the seminar.

Name of the Student : **ABINESH M&RAVICHANDIRAN P**
Register Number : **422118106001 &422118106013**
Year/Semester : **III/V**
DateandTime : **06.07.2020 @ 3;30 pm**
Topic of the Seminar : **" VIRTUAL MEMORY"**

HOD

Power Point Slides

VIRTUAL MEMORY

VIRTUAL MEMORY

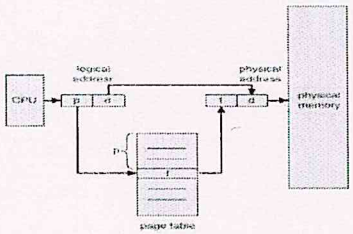
- ***Virtual memory is a memory management capability of an operating system that uses hardware and software to allow a computer to compensate for physical memory shortages by temporarily transferring data from RAM to disk storage.***
- The concept of virtual memory in computeorganisationis allocating memory from the hard disk and making that part of the hard disk as a temporary RAM.
- In other words, it is a technique that uses main memory as a cache for secondary storage.
- The motivations for virtual memory are:
- To allow efficient and safe sharing of memory among multiple programs
- To remove the programming burdens of a small, limited amount of main memory.

- Virtual memory provides an illusion to the users that the PC has enough primary memory left to run the programs.
- Sometimes the size of programs to be executed may sometimes very bigger than the size of primary memory left, the user never feels that the system needs a bigger primary storage to run that program.
- When the RAM is full, the operating system occupies a **portion of the hard disk and uses it as a RAM.**
- In that part of the secondary storage, the part of the program which not currently being executed is stored and all the parts of the program that are executed are first brought into the main memory.
- This is the theory behind **virtual memory.**

Addressing in virtual memory

- A virtual address is considered as a pair (p,d) where lower order bits give an offset d within the page and high -order bits specify the page p.
- The job of the Memory Management Unit (MMU) is to translate the page number p to a frame number f.
- The physical address is then (f,d), and this is what goes on the memory bus.

Conversion of logical address to physical address

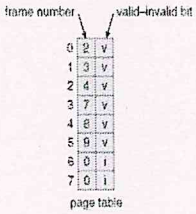


Page table

- The following are the entries in page tables:
- 1. Validity bit: Set to 0 if the corresponding page is not in memory
- 2. Frame number: Number of bits required depends on size of physical memory

Role of control bit in page table

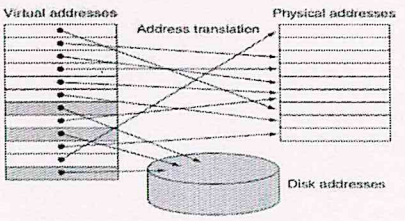
- The control bit (v) indicates whether the page is loaded in the main memory.
- It also indicates whether the page has been modified during its residency in the main memory.
- This information is crucial to determine whether to write back the page to the disk before it is removed from the main memory during next page replacement.



Working mechanism

- In virtual memory, blocks of memory are mapped from one set of addresses (virtual addresses) to another set (physical addresses).
- The processor generates virtual addresses while the memory is accessed using physical addresses.
- Both the virtual memory and the physical memory are broken into pages, so that a virtual page is really mapped to a physical page.

Mapping of virtual and physical memory



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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

STUDENT SEMINAR

EVALUATION SHEET

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Register Number : **422118106001 &422118106013**

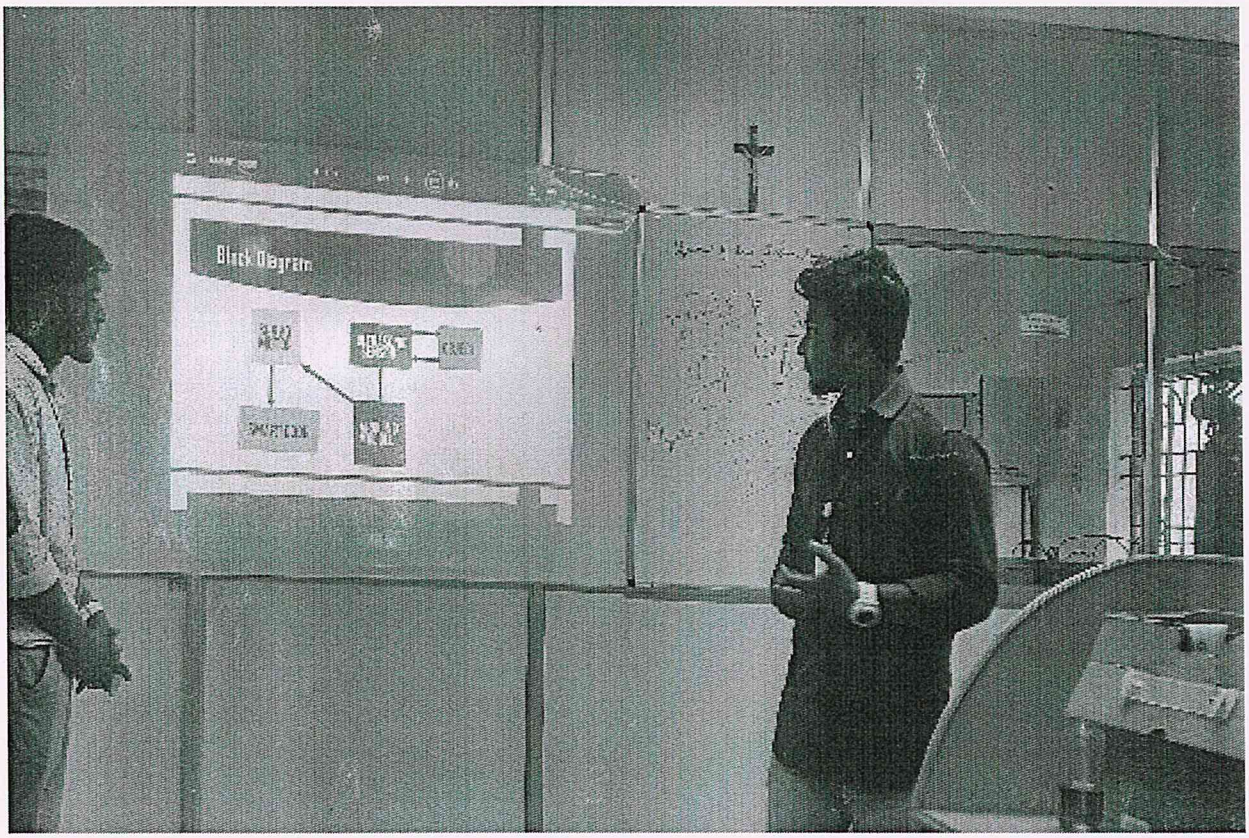
Year/Semester : **III/V**

DateandTime : **06.07.2020 @ 3;30 pm**

Topic of the Seminar : **" VIRTUAL MEMORY"**

	PRESENTATION CONTENT (10)	PRESENTATION STYLE (10)	ANSWERING QUESTIONS (10)	REMARKS
Class Incharge	9	8	8	Good.
HOD	8	8	7	Improved.

PHOTO



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DEPARTMENT OF ELECTRCAL AND ELECTRONICS ENGINEERING

CIRCULAR
(2020-2021)

CIR. No.: SACET/EEE/CIR/ 18 **Date: 04.09.2020**

It is to be informed that the **Student Seminar** will be held for III/6 year/sem students at Machine Lab. All interested staff and students are invited for the seminar.

Name of the Student : **KRISHNA KUMAR SM**
Register Number : **422117105304**
Year/Semester : **IV/8**
DateandTime : **06.09.2020 @ 3;30 pm**
Topic of the Seminar : "**How to create a basic webpage using HTML"**

HOD

Power Point Slides

How to create a basic webpage using HTML

Scope

- What is HTML?
- What are the tools needed for creating web pages using HTML?
- What are the basic HTML tags?
- How to create a web page using HTML?
- How to build a web site using HTML?

What is HTML?

- Hyper Text Markup Language (HTML), the publishing language of the World Wide Web; the standard used to create web pages
- Markup language that defines the
  - structure of information by using a variety of tags and attributes,
  - designed to display text and other information on a screen and
  - provide hyperlinks to other Web documents

Things we need to make a webpage

- Notepad
- Browser
- HTML Tags

Browsers

Browser

Google Chrome

Browser

Internet Explorer

Browser

Mozilla Firefox

HTML Tags

< HTML>

How To Make ABasic Webpage Using Notepad And HTML Tags

What are the basic HTML rules?

1. HTML tags are enclosed by brackets < > for example <HTML>
2. Most tags require a closing tag <HTML> … </HTML>
3. Tags must be nested correctly <B><I>My Library Web Site</I></B> first tag on, last tag off
4. HTML treats all white space as a single blank space

Creating a Basic Starting Document

```
<HTML>
  <HEAD>
    <TITLE>ANNA University</TITLE>
  </HEAD>
  <BODY>
   This is what is displayed
  </BODY>
</HTML>
```

These tags generally define the basic structure of a web page

```
<HTML>  [identifies the document as HTML]
<HEAD>
<TITLE>My Library</TITLE> [ Contains information about the document]
</HEAD>
<BODY>
<P>Content of My Library's Web Page</P>
</BODY>  [Contains all information displayed on the browser]
</HTML> [closing tag ]
```

HTML TAGS

- <html> - This is the root element of an HTML page.
- <head> - Defines information about the document.
- <title> - Defines a title for the document.
- <body> - Defines the document's body.
- <h1> to <h6> - Defines HTML headings.

HTML Tags…

- <p> - Defines a paragraph.
-
 - Defines a single line break.
- - Defines centered text.
- <hyperlink>
- <img src="Picture.jpg"> inserts picture

My first web page

The basic HTML tags above (written using Notepad and saved with a file extension . htm or .html) create a simple web page shown beside it.

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STUDENT SEMINAR

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	PRESENTATION CONTENT (10)	PRESENTATION STYLE (10)	ANSWERING QUESTIONS (10)	REMARKS
Class Incharge	8	10	7	Good
HOD	8	9	6	Explore more

PHOTO